

“2024, Año de Felipe Carillo Puerto, Benemérito del Proletariado,
Revolucionario y Defensor del Mayab”



The Center for Research and Advanced Studies of the National Polytechnic Institute at Guadalajara Campus (Cinvestav-Guadalajara) invites all interested individuals to apply for a Research Professor position assigned to the area of Electronic Design. This announcement aims to attract top talent in integrated circuit design, emphasizing the importance of diversity and innovation in teaching and research.

■ **Characteristics and Requirements:**

We are seeking a qualified individual to enhance and support the research initiatives in Electronic Design. The ideal candidate will have a strong background in hardware design, either analog or digital, and will also possess expertise in electrical testing.

■ **Requirements**

- Doctoral degree in Electrical Engineering or a related field. Candidates holding a doctoral degree awarded by our institution who have demonstrated an exceptional track record during their doctoral studies and have the recommendation of more than one-third of their thesis committee are eligible to participate in this call. Those with a doctoral degree from other institutions may apply without this experience requirement.
- Strong background in teaching and research.
- A robust research portfolio supported by publications in journals and/or conferences.
- Female candidates under 38 and male candidates under 35 are particularly encouraged to apply.

■ **Key Competencies**

- Proficiency in CAD tools such as AWR, ADS, Cadence, GoldenGate, Mentor, Synopsys, or SystemVue.
- Proficiency in hardware-level design of analog, digital, or high-frequency circuits, with experience in integrated or discrete levels.
- Knowledge of electrical testing for analog and digital electronic circuits, with experience in either on-wafer or discrete levels.

■ **We Highly Value**

- Knowledge of or willingness to acquire expertise in emerging areas that could advance the future development of electronic circuit design.
- Proficiency in Spanish for teaching and communication and fluency in English.
- An open-minded, collaborative attitude and the ability to lead projects effectively.

■ Salary

According to the assigned category, following the curriculum evaluation of the selected individual, and based on the Statute of the Promotion and Incentives Commission for Researchers of Cinvestav (COPEI). **The Internal Review Committee of Cinvestav will assign the category.**

■ Required Application Documents

Interested candidates should send:

- A cover letter highlighting professional experience and qualifications relevant to the position.
- Curriculum Vitae highlighting the most significant publications.
- Proposed research program.
- Names and contact information of three references.

Applications should be submitted in PDF format to Dr. J. R. Loo-Yau (raul.loo@cinvestav.mx).

■ Important Dates

- Submission of applications: until January 10, 2025.
- Evaluation processes: January 27 - January 31, 2025
- Results: Second week of February 2025.